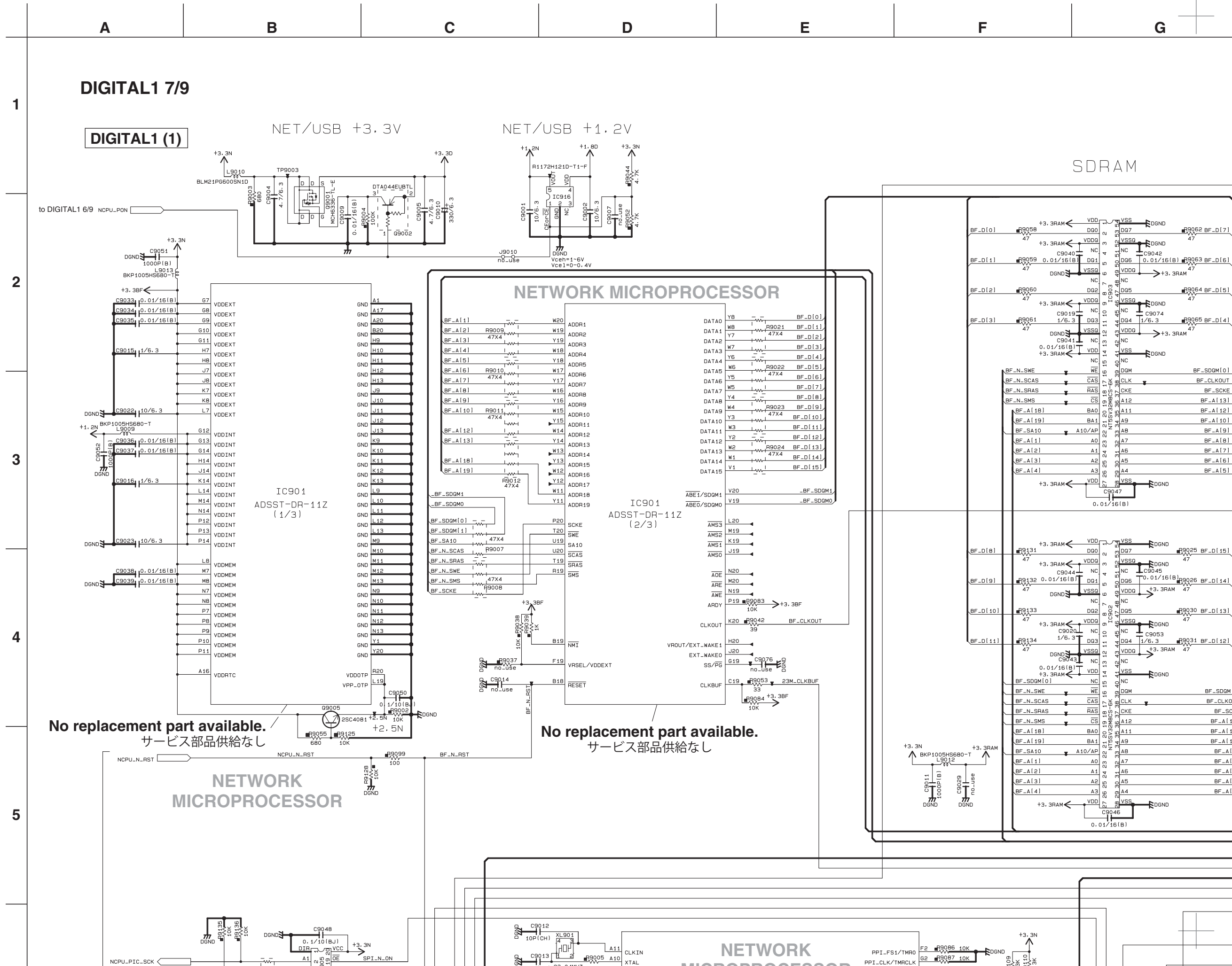
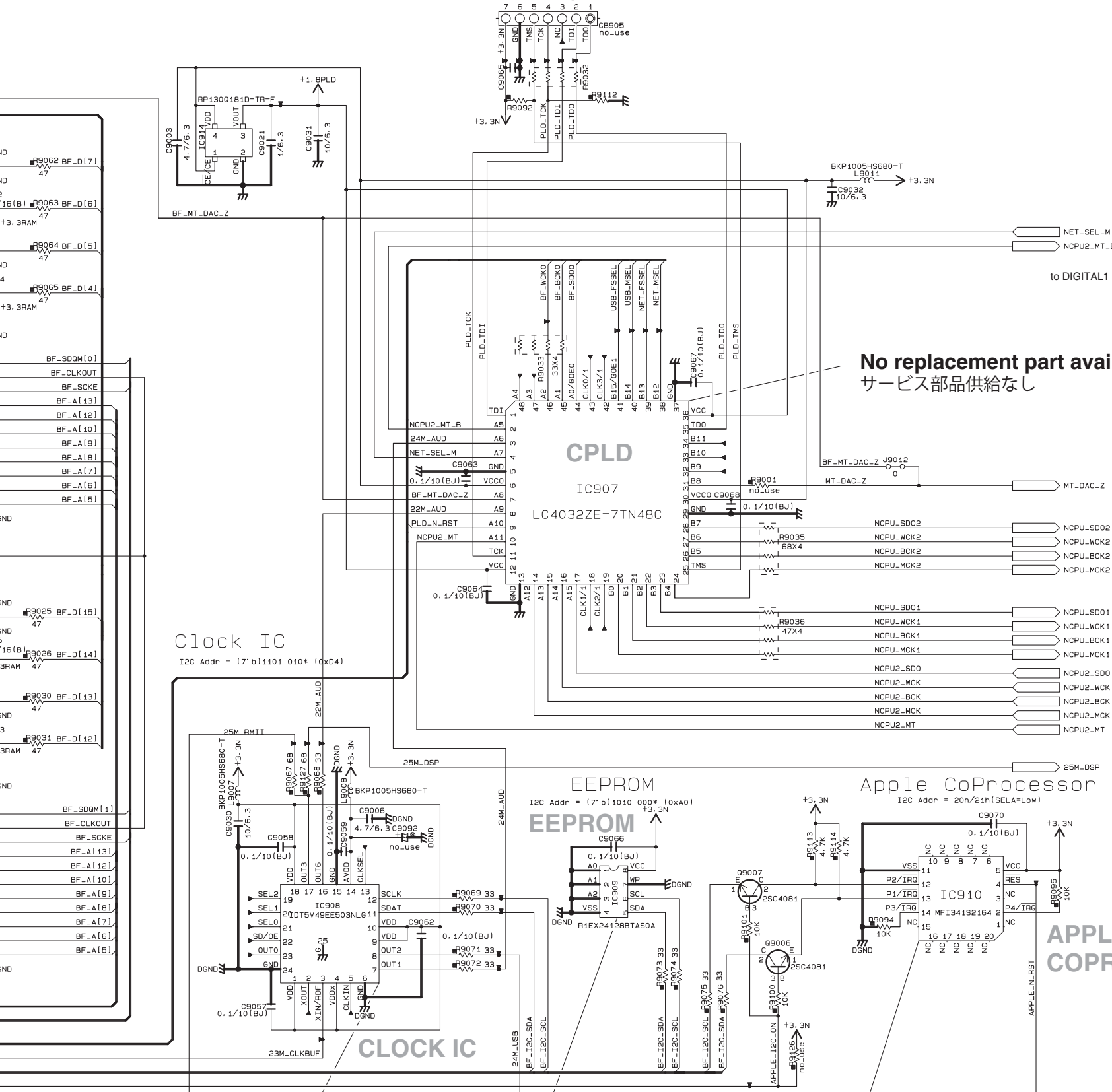




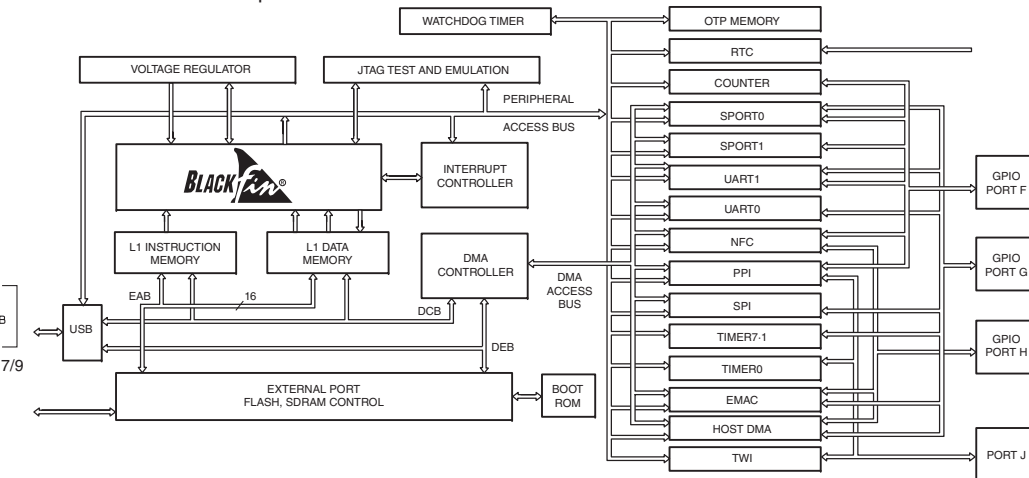
A	B
C	D

RX-A3010/RX-V3071

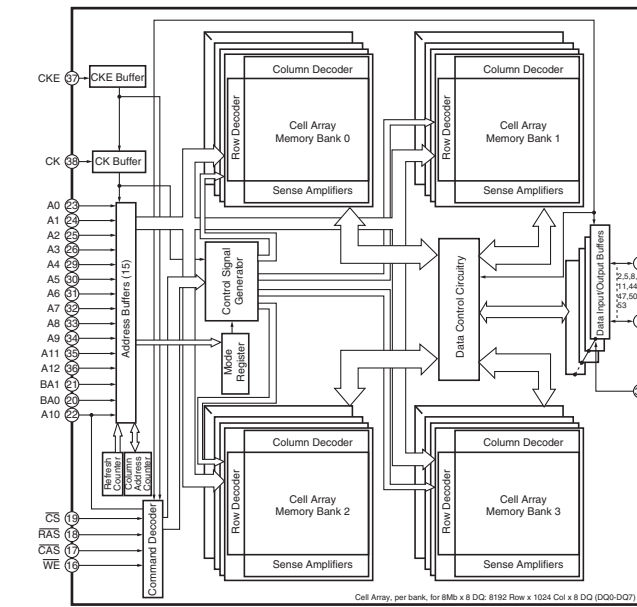
CPLD Configuration



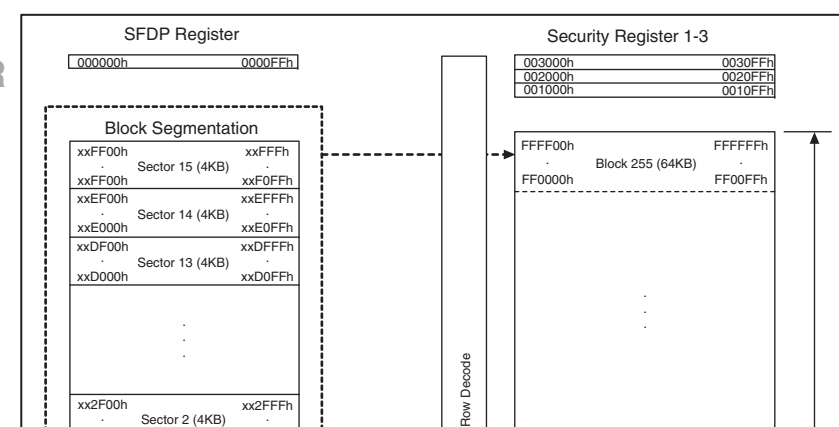
IC901: ADSST-DR-11Z
Blackfin embedded processor



IC902, 903: NT5SV32M8CS-6K
256 Mb x 8 SDRAM



IC904: W25Q128BVFIFG
128 M-bit serial flash memory



to DIGITAL1 6/9

7

8

NOTICE (model)
(J)..... JAPAN
(U)..... U.S.A
(C)..... CANADA
(R)..... GENERAL
(T)..... CHINA
(K)..... KOREA
(A)..... AUSTRALIA
(B)..... BRITISH
(G)..... EUROPE
(L)..... SINGAPORE
(E)..... SOUTH EUROPE
(V)..... TAIWAN
(F)..... RUSSIAN
(P)..... LATIN AMERICA
(S)..... BRAZIL
(H)..... THAI

REMARKS	PARTS	NAME
NO MARK	CARBON FILM RESISTOR (P=5)	
□	CARBON FILM RESISTOR (P=10)	
△	METAL OXIDE FILM RESISTOR	
▲	METAL FILM RESISTOR	
⊠	METAL PLATE RESISTOR	
⊞	FIRE PROOF CARBON FILM RESISTOR	
□	CEMENT MOLDED RESISTOR	
⊘	SEMI VARIABLE RESISTOR	
■	CHIP RESISTOR	

REMARKS	PARTS	NAME
NO MARK	ELECTROLYTIC CAPACITOR	
⊗	TANTALUM CAPACITOR	
NO MARK	CERAMIC CAPACITOR	
●	CERAMIC TUBULAR CAPACITOR	
◎	POLYESTER FILM CAPACITOR	
○	POLYSTYRENE FILM CAPACITOR	
①	MICA CAPACITOR	
⊙	POLYPROPYLENE FILM CAPACITOR	
●	SEMICONDUCTIVE CERAMIC CAPACITOR	

- ★ Components having special characteristics are marked △ and must be replaced with parts having specifications equal to those originally installed.
- ★ Schematic diagram is subject to change without notice.

- △印のある部品は、安全性確保部品を示しています。部品の交換が必要な場合、パーツリストに記載されている部品を使用してください。
- 本回路図は標準回路図です。改良のため予告なく変更することがあります。

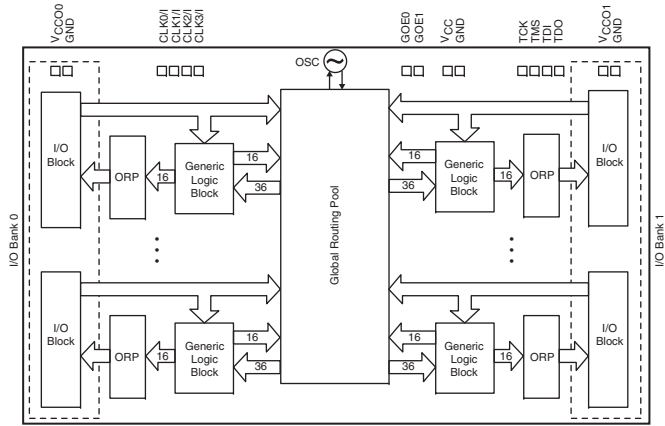
10

RX-A3010/RX-V3071

A	B
C	D

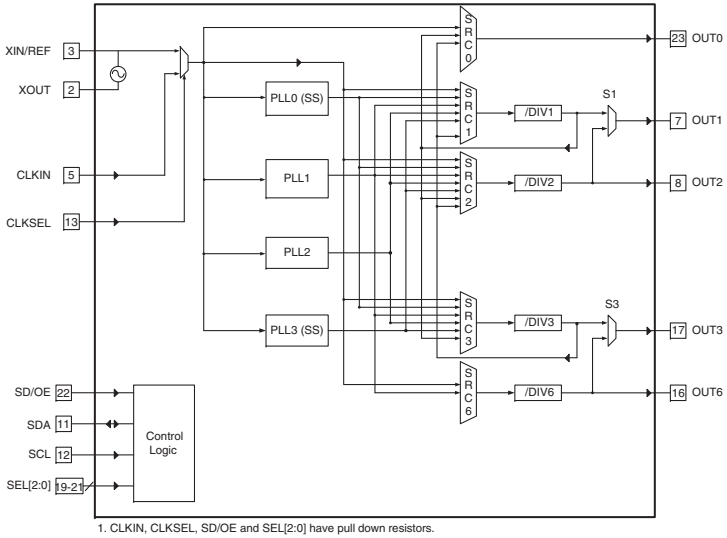
DIGITAL1 7/9

IC907: LC4032ZE-7TN48C
1.8 V in-system programmable ultra low power PLDs



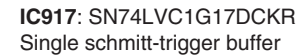
No replacement part available.
サービス部品供給なし

IC908: 5V49EE503-027NLG18
Clock synthesizer



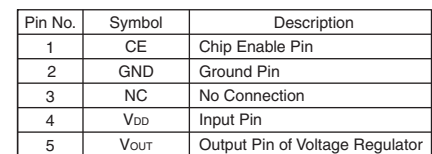
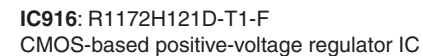
サービス部品供給なし

サービス部品供給なし





INPUT A	OUTPUT Y
H	H
L	L



RX-A3010/RX-V3071

A	B
C	D



Differential Impedance : $90\ \Omega$

USB Page 139 H7
to OPERATION (2)_W4501

The block diagram illustrates the internal architecture of the H8/300H MICROPROCESSOR. The central component is the H8/300H MICROPROCESSOR, which includes System control, Security logic, and an I/O port. The I/O port is connected to external pins P4/I/O, P3/I/O, P2/I/O, and P1/I/O. The processor is also connected to a Voltage monitoring circuit, which includes a RING, WDT, and FMU. The internal address bus connects the processor to the ROM (112 kbytes), RAM (4 kbytes), EEPROM (16 kbytes + 2 kbytes), Interval timer 1, Interval timer 2, and a Modular multiplication coprocessor. The coprocessor is also connected to a Register (512 bytes). The power-on reset circuit is connected to the RES pin, and the on-chip oscillator is connected to the internal address bus. The top view shows the pin configuration: NC (1), P4/I/O (2), NC (3), RES (4), Vcc (5), 6, 7, 8, 9, 10, 11 (Vss), 12 (P2/I/O), 13 (P1/I/O), 14 (P3/I/O), 15 (NC), 16, 17, 18, 19, 20.

Pin No.	Symbol	Description
1	CE	Chip Enable ("H" Active)
2	GND	Ground Pin
3	VOUT	Output Pin
4	VDD	Input Pin

Pin No.	Symbol	Description
1	CE	Chip Enable ("H" Active)
2	GND	Ground Pin
3	V _{OUT}	Output Pin
4	V _{DD}	Input Pin

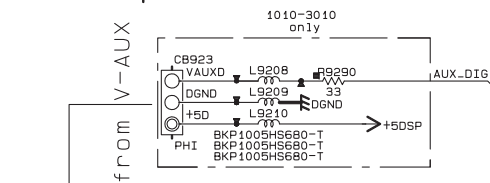
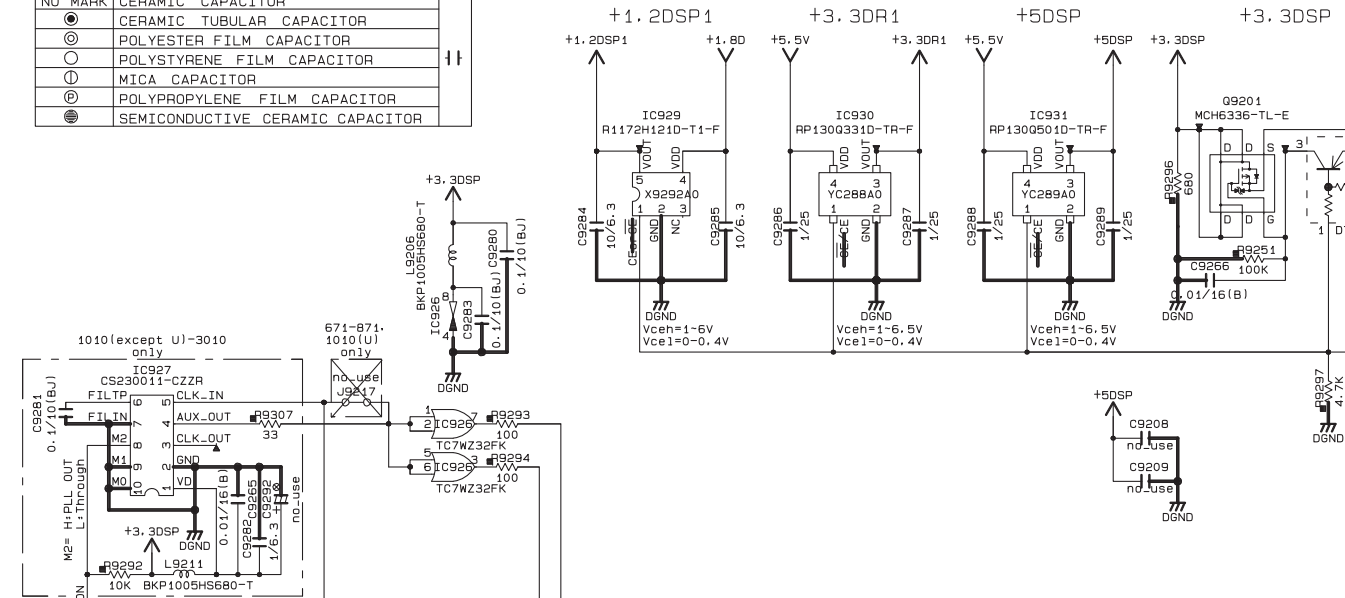
The block diagram shows the control logic for the 16C46. The control logic block is connected to VCC (pin 6), VSS (pin 4), WP (pin 7), A0, A1, A2 (pins 1, 2, 3), SCL (pin 6), and SDA (pin 5). It controls a High voltage generator, which is connected to a Memory array. The Memory array is connected to a Y-select and Sense amp, which is connected to a Serial-parallel converter. The Serial-parallel converter is connected to the SDA pin.

Destination Part List			
SXX	LOC	JCRTKABGLF	U
S61	IC94	X	X8398A0 TC7SETH0FU
S62	IC95	X	X8680A0 TC7SH08FU
S63	R974	X	RD45433 33
S64	R840	X	RD45433 33
S65	R889	X	RD45B10 100K
S66	R897	RD45630 3K	RD45647 4.7K
S81	C9212	UF43710 10/16	UF03710 10/16
S91	R9421	X	RD45410 10
S92	R9422	X	RD45B10 100K
S93	Q9402	X	W87B190 DTA043EUBTL
S94	Q9401	X	W87B230 DTC044EUBTL
S95	C9402 C9434	UF43710 10/16	UF03710 10/16
S96	C9413	US63410 1000P(B)	US63410 0.01/16(B)
S97	R9419	W852070 1P6.8	W87B470 1P6.8

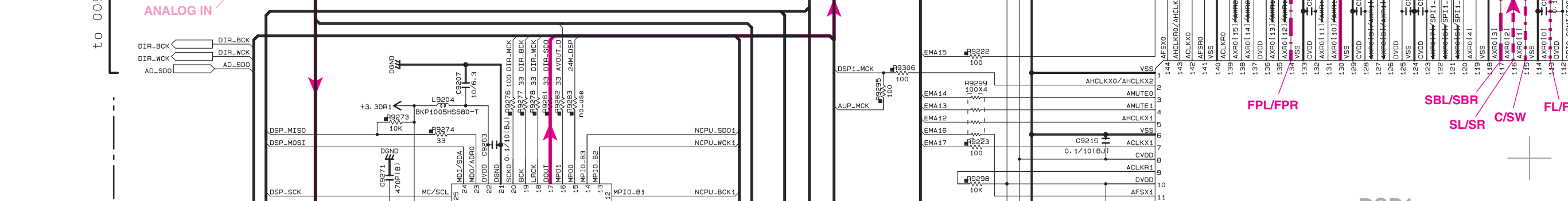
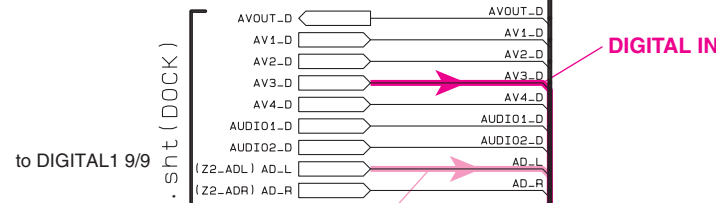
RESISTOR	
REMARKS	PARTS NAME
NO MARK	CARBON FILM RESISTOR (P=5)
	CARBON FILM RESISTOR (P=10)
	METAL OXIDE FILM RESISTOR
	METAL FILM RESISTOR
	METAL PLATE RESISTOR
	FIRE PROOF CARBON FILM RESISTOR
	CEMENT MOLDED RESISTOR
	SEMI VARIABLE RESISTOR
	CHIP RESISTOR

CAPACITOR		
REMARKS	PARTS NAME	
NO MARK	ELECTROLYTIC CAPACITOR	H
⊗	TANTALUM CAPACITOR	
NO MARK	CERAMIC CAPACITOR	H
⊙	CERAMIC TUBULAR CAPACITOR	
⊗	POLYESTER FILM CAPACITOR	
○	POLYSTYRENE FILM CAPACITOR	
⌒	MICA CAPACITOR	
⊕	POLYPROPYLENE FILM CAPACITOR	
⊗	SEMICONDUCTIVE CERAMIC CAPACITOR	

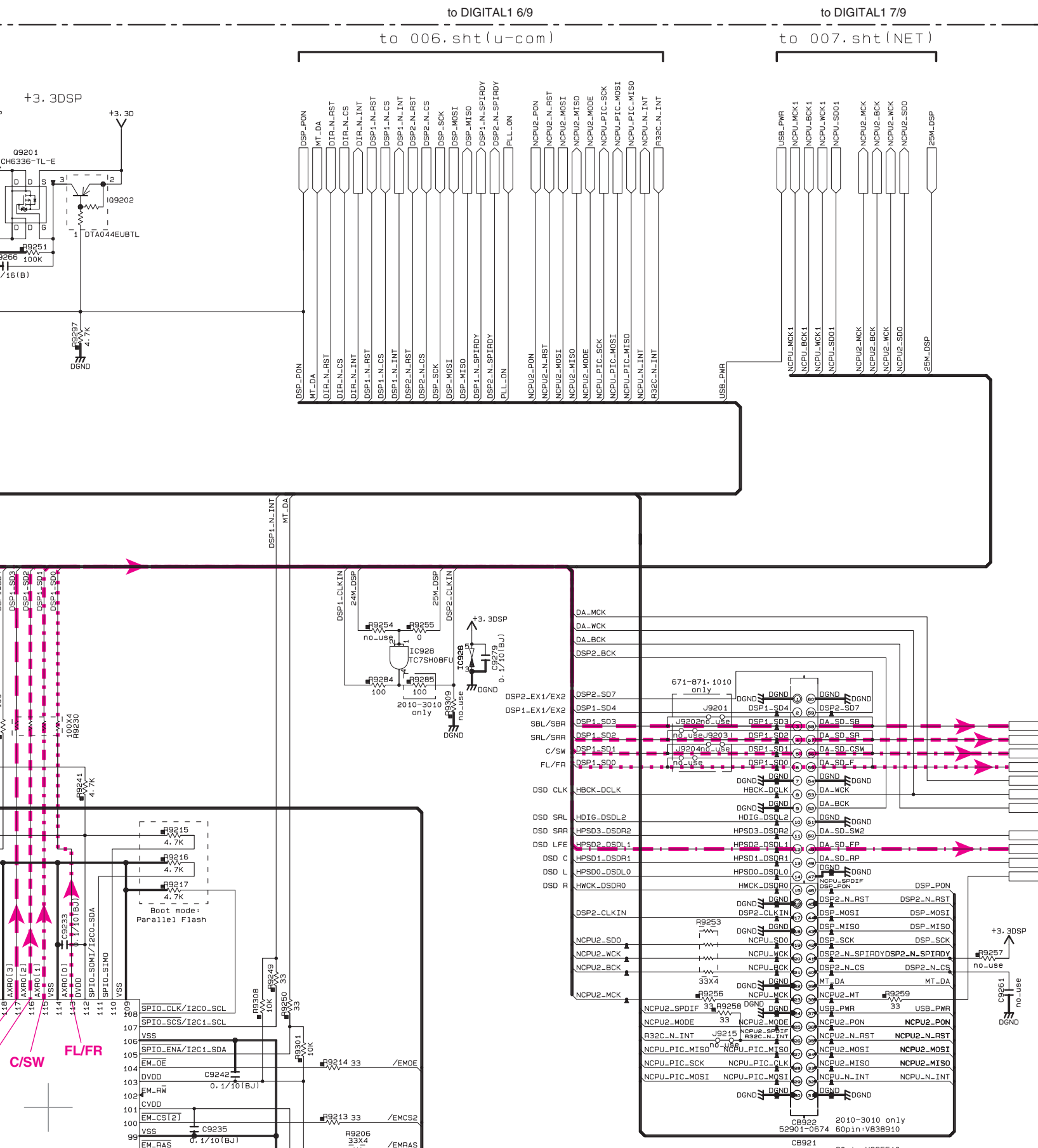
(J)..... JAPAN
(U)..... U. S. A
(C)..... CANADA
(R)..... GENERAL
(T)..... CHINA
(K)..... KOREA
(A)..... AUSTRALIA
(B)..... BRITISH
(G)..... EUROPE
(L)..... SINGAPORE
(E)..... SOUTH EUROPE
(V)..... TAIWAN
(F)..... RUSSIAN
(P)..... LATIN AMERICA
(S)..... BRAZIL
(H)..... THAI



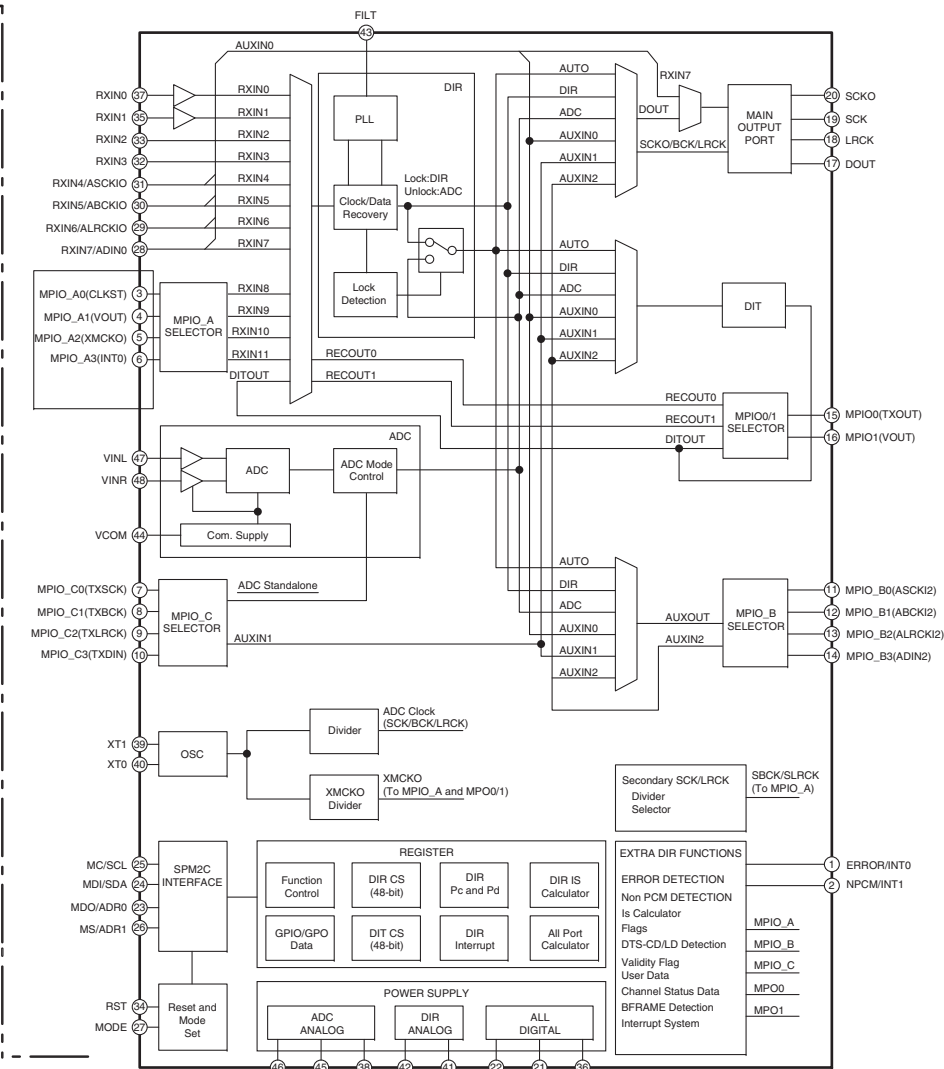
Page 146 12
to DAC_W852



A	B
C	D

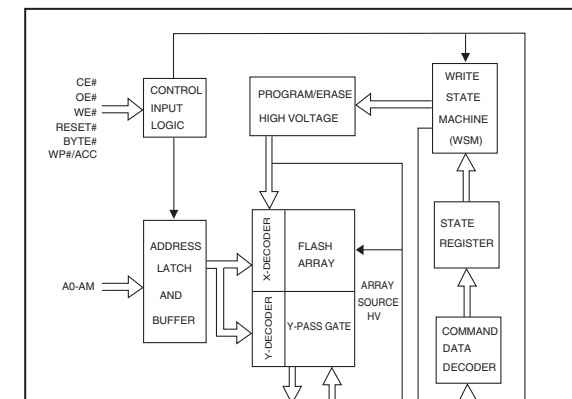


IC924: PCM9210PTR
216-kHz digital audio interface transceiver (DIX) with stereo ADC and routing



to DIGITAL1 9/9

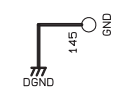
IC923: MX29LV160DBTI-70G
16 M-bit 3 V supply flash memory



SL/SR C/SW



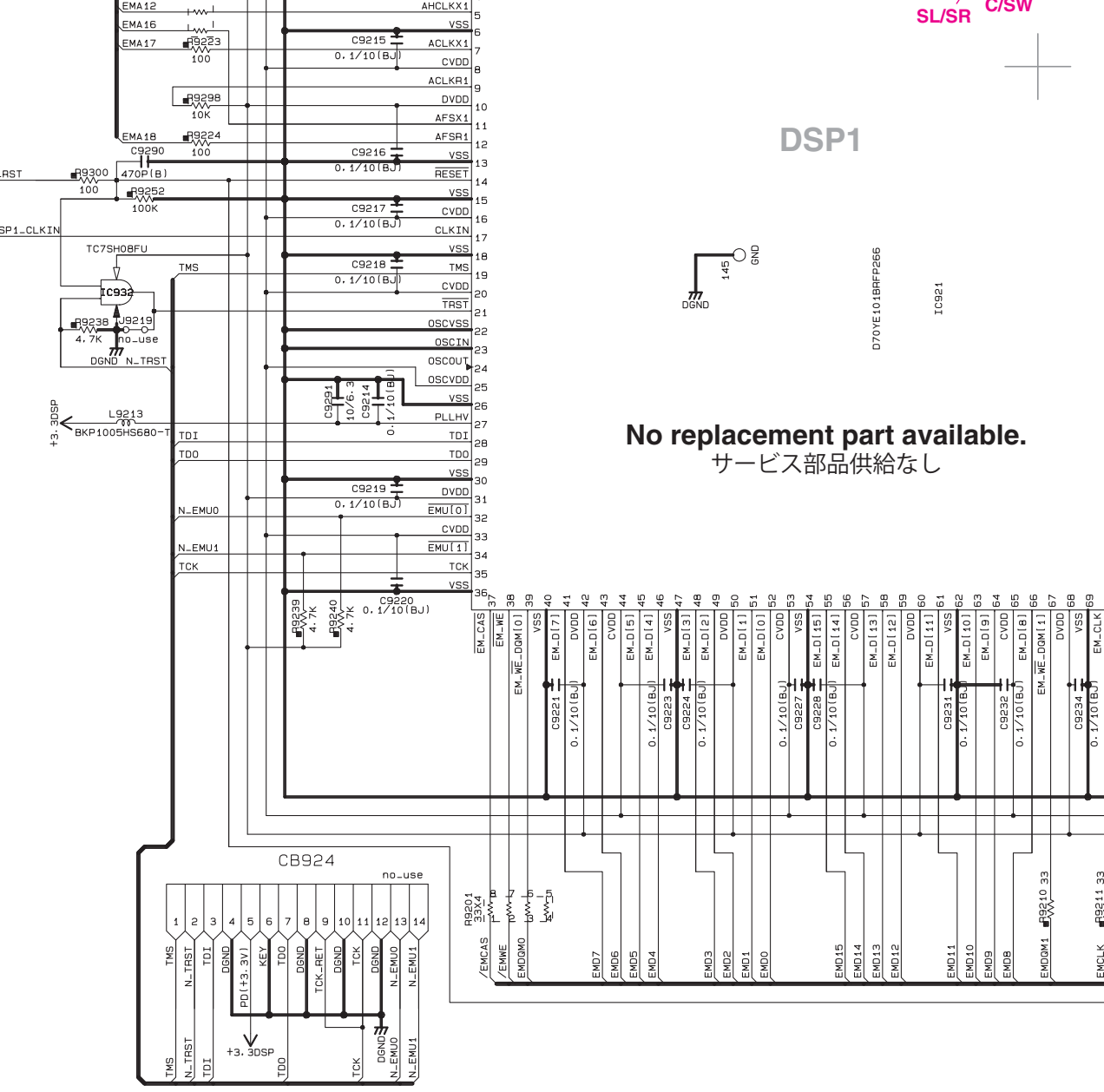
DSP1



D70YE101BRFP266

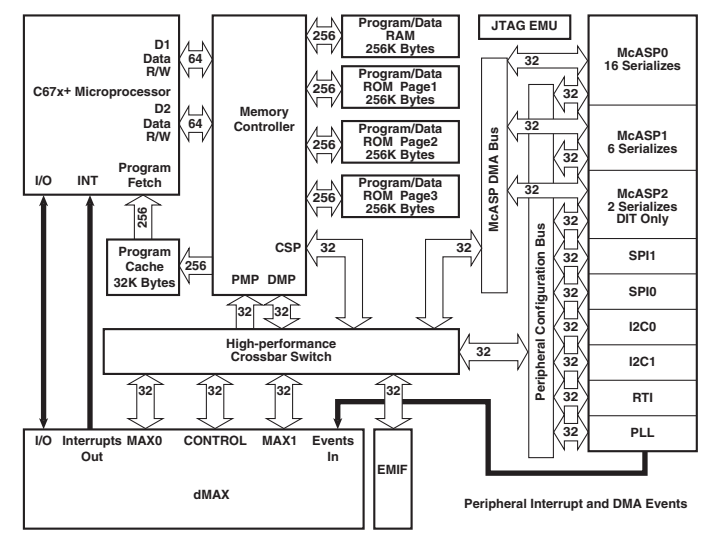
IC921

No replacement part available.
サービス部品供給なし

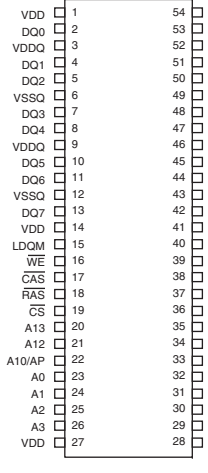
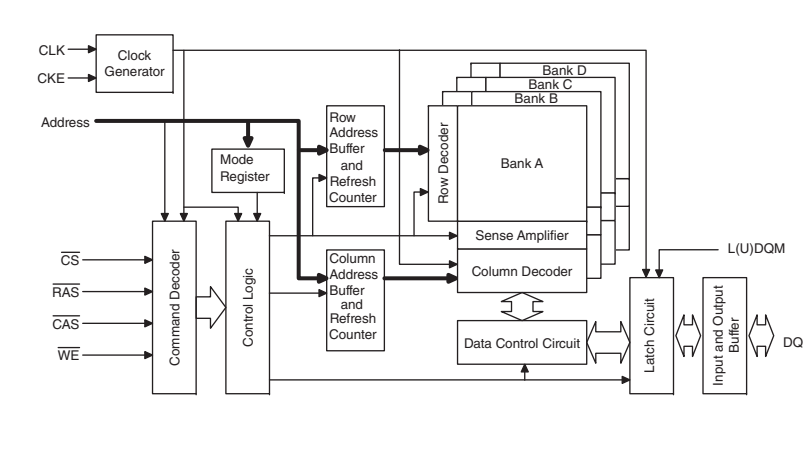


to 005.sht (HDMI Tx)
to DIGITAL1 5/9

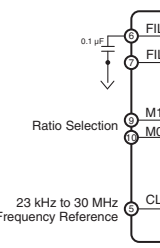
IC921: D70YE101BRFP266 Floating-point digital signal processors



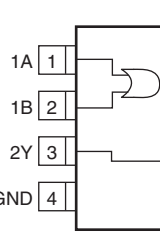
IC922: M12L64164A-5TG 1M x 16-bit x 4 banks synchronous DRAM



IC927: CS2300 Clock multiplier



IC926: TC7WZ3 Dual 2-input OR



6

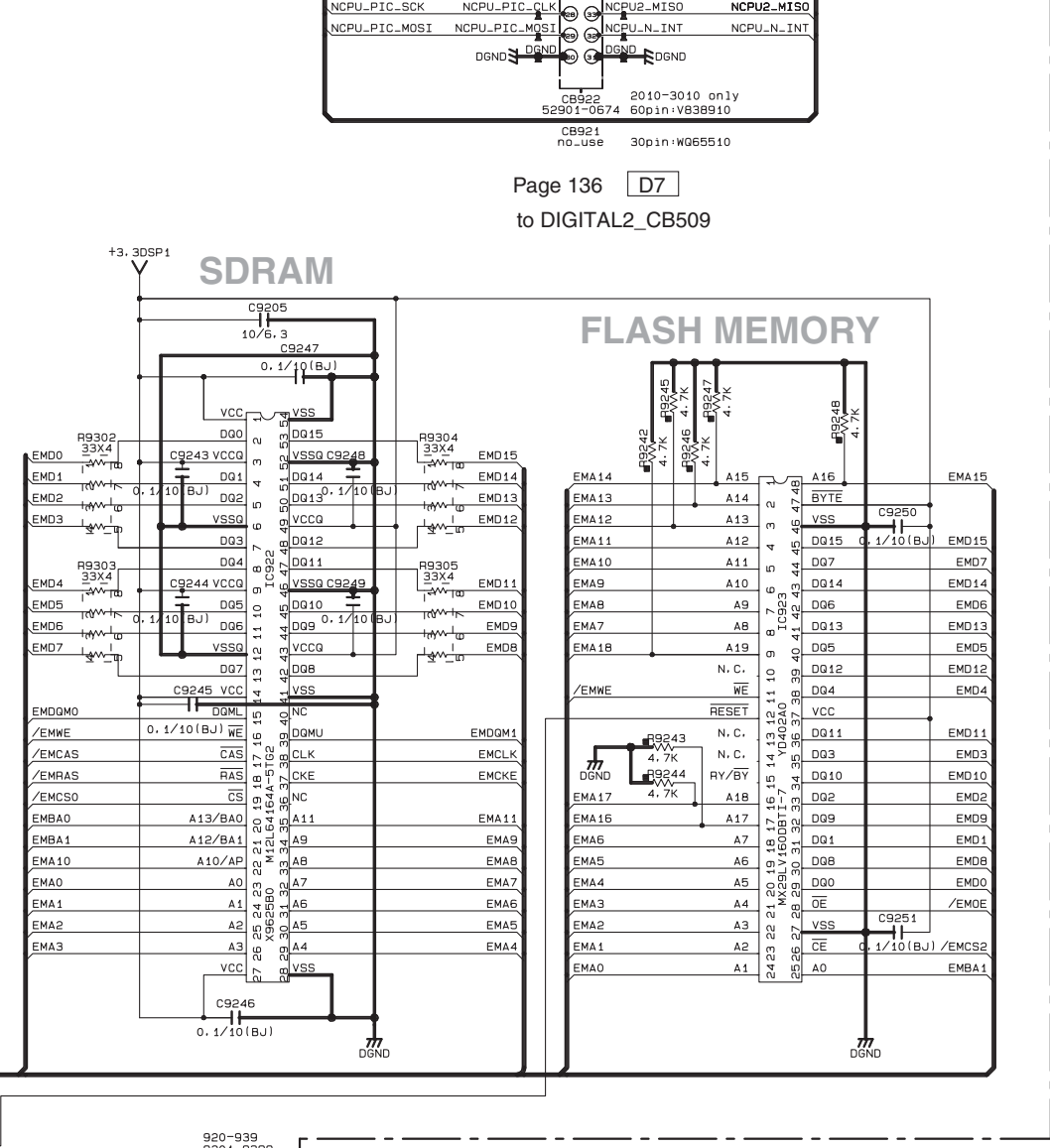
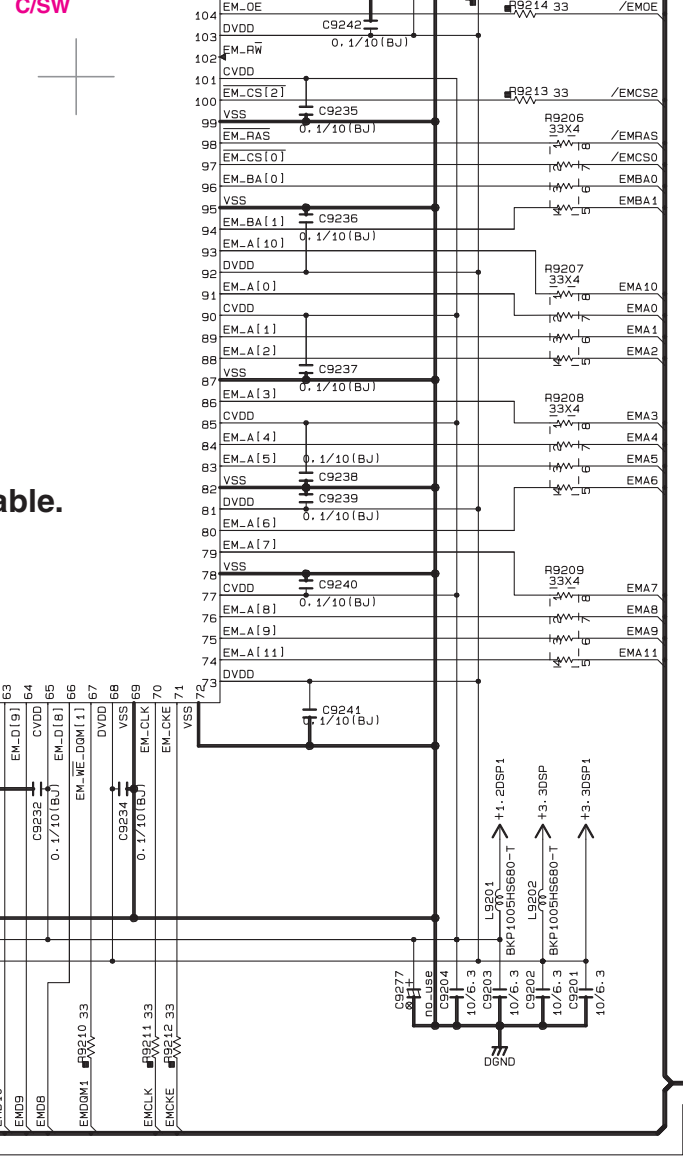
7

8

9

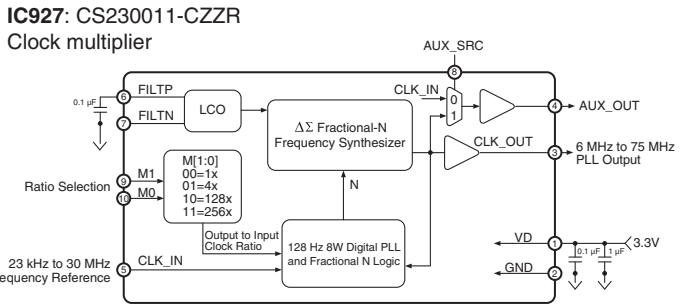
10

A	B
C	D

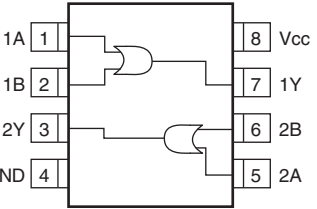


DIGITAL1 (1)

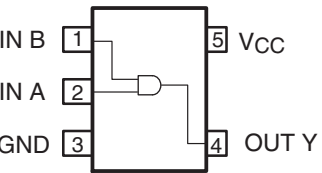
DIGITAL8: DIR1&DSP1



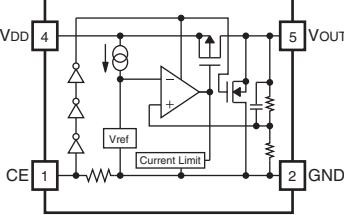
IC926: TC7WZ32FK (TE85L, F)
Dual 2-input OR gate



IC928, 932: TC7SH08FU
2-input AND gate

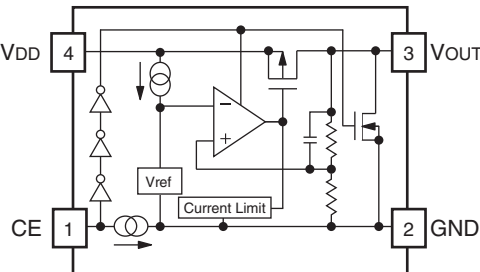


IC929: R1172H121D-T1-F
CMOS-based positive-voltage regulator IC



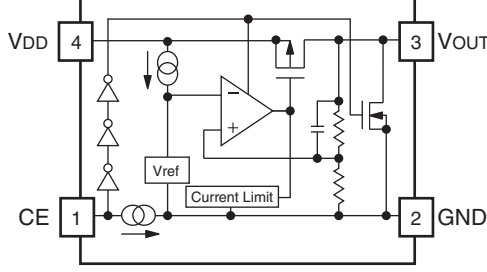
Pin No.	Symbol	Description
1	CE	Chip Enable Pin
2	GND	Ground Pin
3	NC	No Connection
4	VDD	Input Pin
5	VOUT	Output Pin of Voltage Regulator

IC930: RP130Q331D-TR-F
Voltage regulator



Pin No.	Symbol	Description
1	CE	Chip Enable ("H" Active)
2	GND	Ground Pin
3	Vout	Output Pin
4	VDD	Input Pin

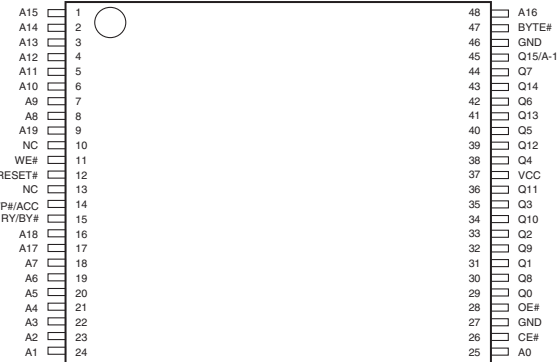
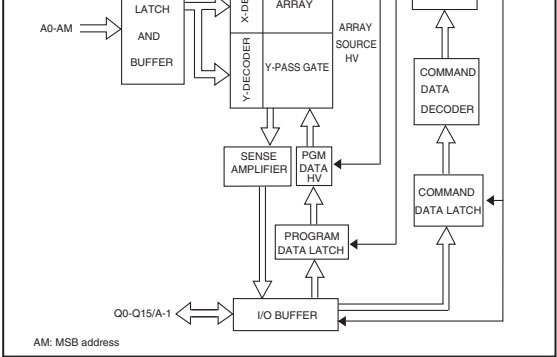
IC931: RP130Q501D-TR-F
Voltage regulator



Pin No.	Symbol	Description
1	CE	Chip Enable ("H" Active)
2	GND	Ground Pin
3	Vout	Output Pin
4	VDD	Input Pin

- ★ Components having special characteristics are marked Δ and must be replaced with parts having specifications equal to those originally installed.
- ★ Schematic diagram is subject to change without notice.

- Δ 印のある部品は、安全性確保部品を示しています。部品の交換が必要な場合、パーツリストに記載されている部品を使用してください。
- 本回路図は標準回路図です。改良のため予告なく変更することがあります。



A	B
C	D

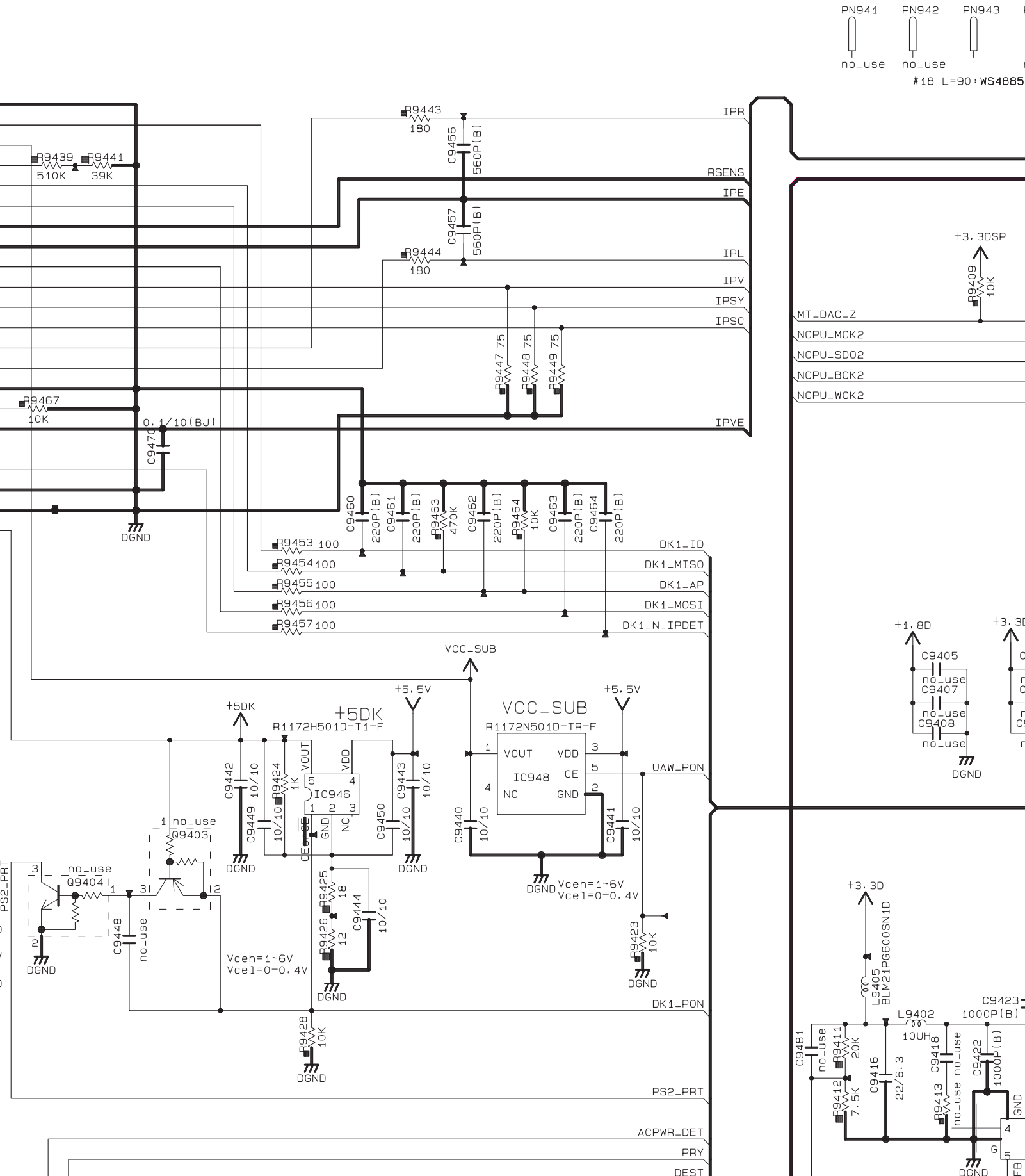
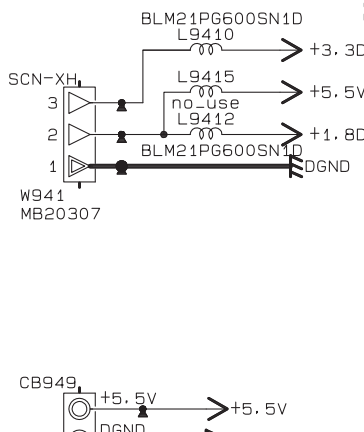
2

3

4

5

TEST





7

8

9

10

- △印のある部品は、安全性確保部品を示しています。部品の交換が必要な場合、パーツリストに記載されている部品を使用してください。
- 本回路図は標準回路図です。改良のため予告なく変更することがあります。



A	B
C	D



DIGITAL9: DOCK

to 008.sht(DSP)
to DIGITAL1 8/9

The diagram illustrates the internal architecture of the PCM1782 audio codec. It features several key functional blocks:

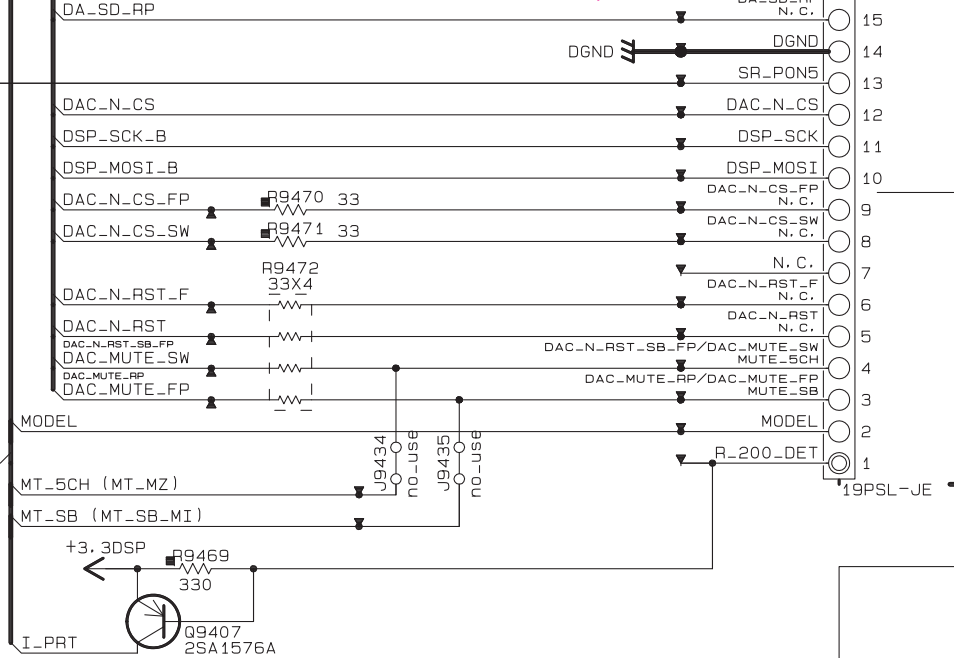
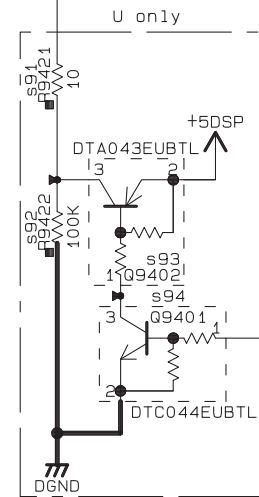
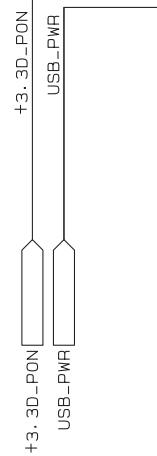
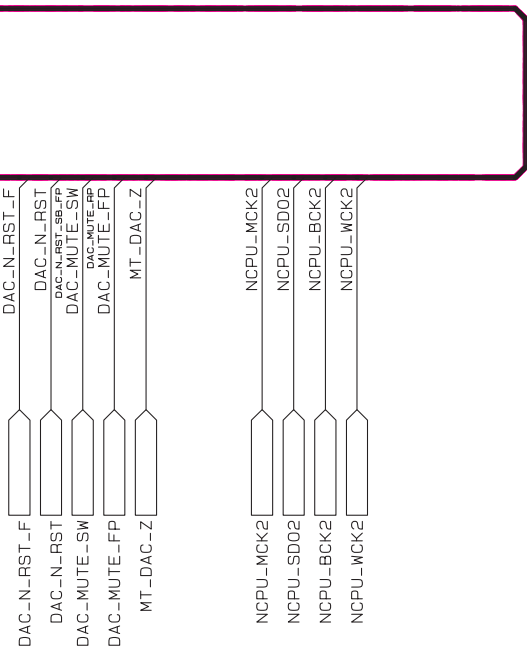
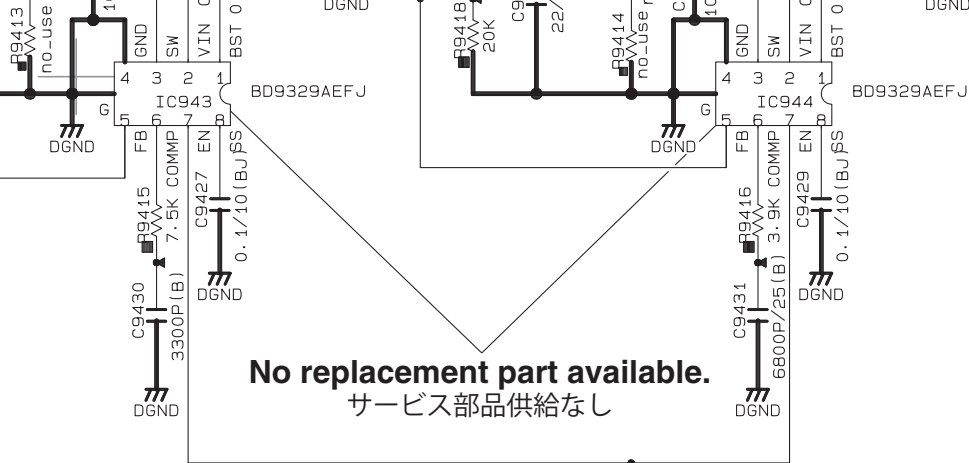
- Audio Serial Port:** Receives BCK, LRCK, and DATA signals.
- Serial Control Port:** Receives FMT, MS (DEMP0), MC (DEMP1), MD (MUTE), and (TEST) signals. It is bidirectionally connected to the Audio Serial Port and the System Clock Manager.
- System Clock Manager:** Receives SCK and provides the System Clock to the Serial Control Port and the Zero Detect block.
- 4 x 8 Oversampling Digital Filter With Function Control:** Receives data from the Serial Control Port and outputs to the Enhanced Multilevel Delta-Sigma Modulator.
- Enhanced Multilevel Delta-Sigma Modulator:** Receives data from the filter and outputs to two DACs.
- DACs (Digital-to-Analog Converters):** Two DACs convert the digital signals to analog.
- Output Amp and Low-Pass Filter:** Two identical blocks that amplify and filter the analog signals from the DACs to produce VOUTL and VOUTR.
- Zero Detect:** Receives the System Clock and provides ZEROLNA(1) and ZERORZA(1) (ZEROA) signals.
- Power Supply:** Provides VCC and AGND connections.

The signal flow is as follows: Digital inputs (BCK, LRCK, DATA, FMT, MS, MC, MD, TEST) and SCK are processed by the Serial Control Port and System Clock Manager. The Serial Control Port sends data to the 4 x 8 Oversampling Digital Filter, which then feeds the Enhanced Multilevel Delta-Sigma Modulator. The modulator's outputs go through two DACs and two Output Amp and Low-Pass Filter blocks to produce the final analog outputs, VOUTL and VOUTR. The Zero Detect block provides status signals ZEROLNA(1) and ZERORZA(1) (ZEROA).

(1) Open-drain output for the PCM1782

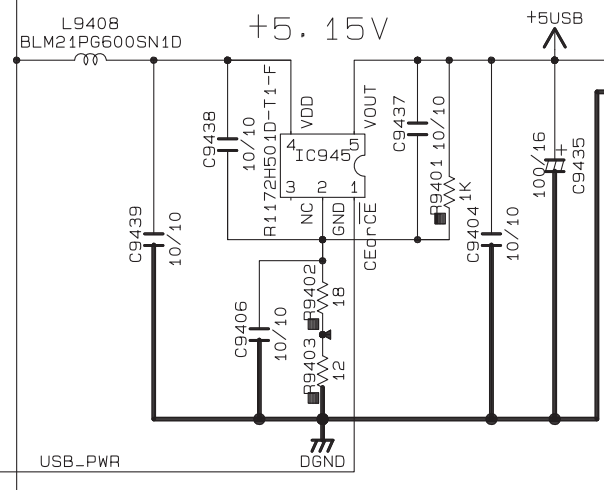
NOTE: Signal names in parentheses () are for the PCM1781.

FMT	1	16	ZEROA
DEMP0	2	15	V _{OUT} L
DEMP1	3	14	V _{OUT} R
MUTE	4	13	V _{COM}
SCK	5	12	AGND
DATA	6	11	V _{CC}
BCK	7	10	NC
LCK	8	9	TEST

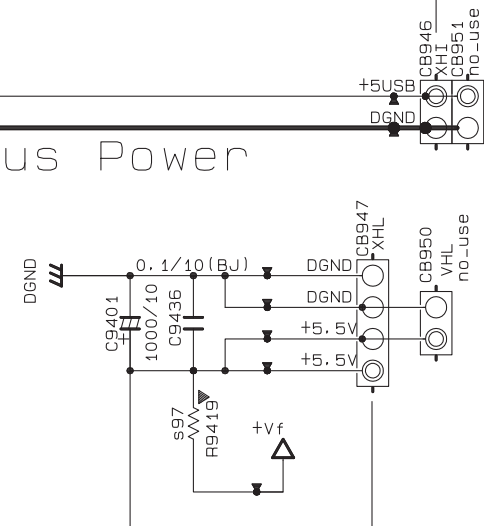


Page 146 **G2**
to DAC_CB854

Page 139 **H7**
to OPERATION (2)_W4502



USB Bus Power

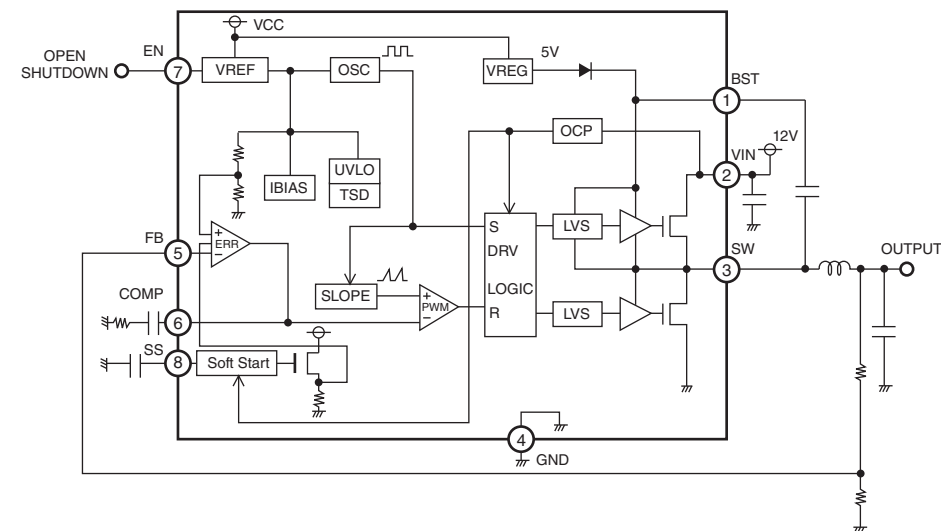


671-871
to VIDEO(2)
1010-3010
to POWER(1)
671-871
to OPE(7)
1010-3010
to OPE(2)

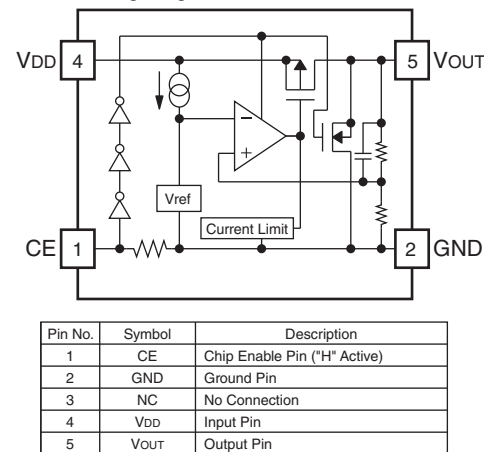
Page 143 **M6**
to POWER_W2001

to 007.sht(BF)
to DIGITAL1 7/9

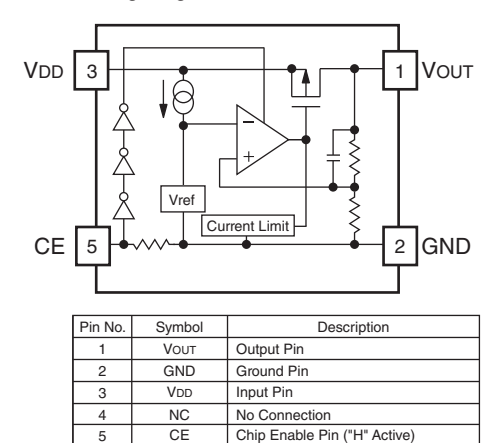
IC943, 944 : BD9329AEFJ-E2
1ch step-down DC/DC converter



IC945, 946: R1172H501D-T1-F
Voltage regulator



IC948: R1172N501D-TR-F
Voltage regulator



IC949: SN74LVC1G17DCKR
Single schmitt-trigger buffer

